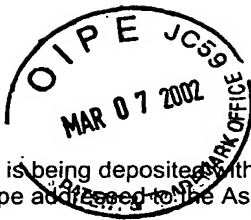


Docket No.: WMP-SME-352



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Date: March 1, 2002

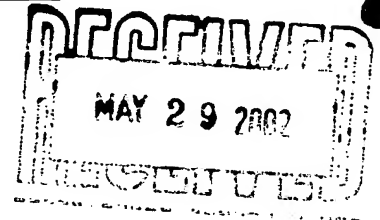
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Klemens Ferstl et al.  
Applic. No. : 10/022,605  
Filed : December 17, 2001  
Title : Multichip Module for LOC Mounting and Method for Producing the Multichip Module  
Art Unit : 2811

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INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231



Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,208,782 (Sakuta et al.), dated May 4, 1993;

United States Patent No. 5,331,201 (Nishino), dated July 19, 1994;

United States Patent No. 5,559,305 (Lee et al.), dated September 24, 1996;

German Published, Non-Prosecuted Patent Application DE 43 01 915 A1 (Michii et al), dated May 6, 1994, multiple chip semiconductor device;


German Published, Non-Prosecuted Patent Application DE 197 04 385 A1 (Möwes), dated August 14, 1997, method for bonding semiconductor chips with an conducting frame and mounting carrier for such a method;

German Published, Non-Prosecuted Patent Application DE 197 12 551 A1 (Kim),  
dated November 20, 1997, supply frame and production method for chip sized  
semiconductor housings based thereon;

International Search Report dated October 10, 2000.

If no translation of pertinent portions of any foreign language patents or publications  
mentioned above is included with the aforementioned copies of those applications,  
patents and/or publications, it is because no existing translation is readily available to  
the applicant.

Respectfully submitted,

  
\_\_\_\_\_  
For Applicants

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Date: March 1, 2002

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